

ABSTRACT OF THE DISCLOSURE

An insulated gate bipolar transistor is disclosed, which comprises a first conductivity type base layer, a second conductivity type base layer and an emitter layer which are selectively formed in an upper surface of the first conductivity type base layer, a buffer layer and a collector layer which are formed on a back surface of the first conductivity type base layer. A requirement of $d_2/d_1 > 1.5$ is satisfied, where d_1 is a depth in the buffer layer, as measured from an interface of the buffer layer and the collector layer, at which a first conductivity type impurity concentration in the buffer layer shows a peak value, and d_2 is a shallowest depth in the buffer layer, as measured from the interface of the buffer layer and the collector layer, at which an activation ratio of the first conductivity type impurity in the buffer layer is a predetermined value.

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